

60V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

$V_{(BR)DSS}$	Max $R_{DS(on)}$	Max I_D $T_A = 25^\circ C$
-60V	400m Ω @ $V_{GS} = -10V$	-1.1A
	600m Ω @ $V_{GS} = -4.5V$	-0.9A

Description

This MOSFET utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed, making it ideal for high-efficiency power management applications.

Applications

- DC - DC converters
- Power management functions
- Relay and solenoid driving
- Motor control

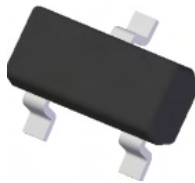
Features

- Fast switching speed
- Low input capacitance
- Low gate charge
- **Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP capable (Note 4)**

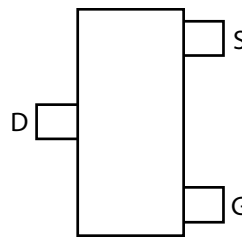
Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208
- Weight: 0.008 grams (approximate)

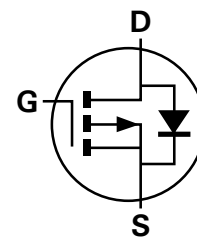
SOT23



Top View



Top View
Pin Out



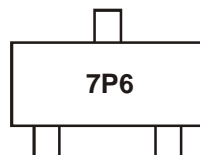
Equivalent Circuit

Ordering Information (Notes 4 & 5)

Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXMP6A13FTA	AEC-Q101	7P6	7	8	3000 Units
ZXMP6A13FQTA	Automotive	7P6	7	8	3000 Units

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See <http://www.diodes.com> for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.
 3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified.
 5. For packaging details, go to our website at <http://www.diodes.com>

Marking Information



7P6 = Product Type Marking Code

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

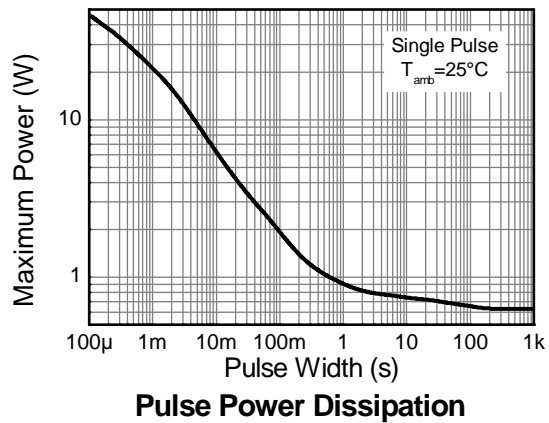
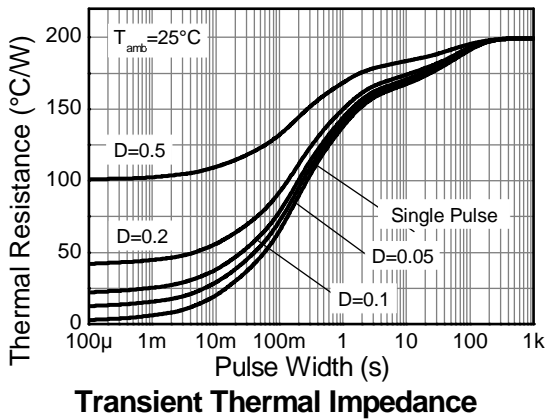
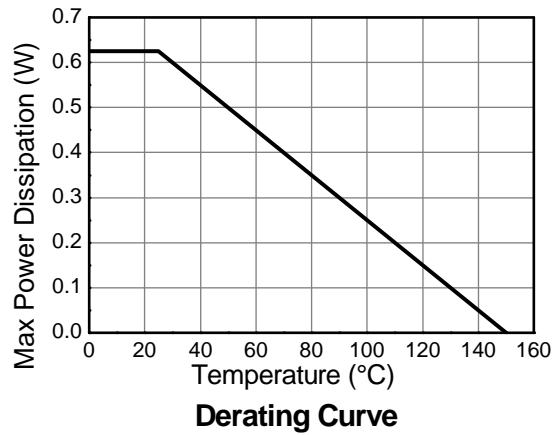
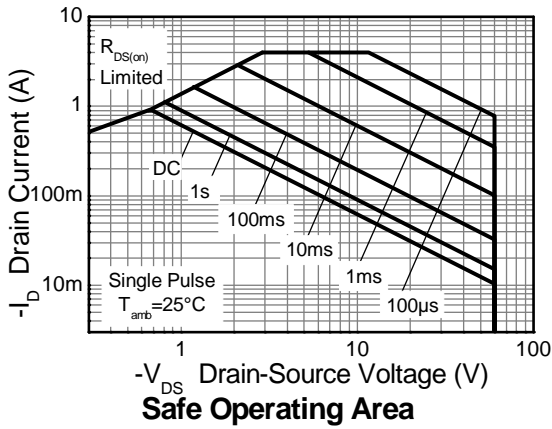
Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	-60	V
Gate-Source Voltage			V _{GS}	±20	V
Continuous Drain Current	V _{GS} = 10V	T _A = 70°C (Note 7)	I _D	-1.1	A
		(Note 7)		-0.8	
		(Note 6)		-0.9	
Pulsed Drain Current (Note 8)			I _{DM}	-4.0	A
Continuous Source Current (Body Diode) (Note 7)			I _S	-1.2	A
Pulsed Source Current (Body Diode) (Note 8)			I _{SM}	-4.0	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Power Dissipation (Note 6)		P _D	625	mW
Linear Derating Factor			5	mW/°C
Power Dissipation (Note 7)		P _D	806	mW
Linear Derating Factor			6.5	mW/°C
Thermal Resistance, Junction to Ambient (Note 6)		R _{θJA}	200	°C/W
Thermal Resistance, Junction to Ambient (Note 7)		R _{θJA}	155	°C/W
Thermal Resistance, Junction to Leads (Note 9)		R _{θJL}	194	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

- Notes:
6. For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions
 7. For a device surface mounted on FR4 PCB measured at t ≤ 5 secs.
 8. Repetitive rating 25mm x 25mm FR4 PCB, D = 0.05 pulse width = 10µs - pulse current limited by maximum junction temperature.
 9. Thermal resistance from junction to solder-point (at the end of the collector lead).

Thermal Characteristics

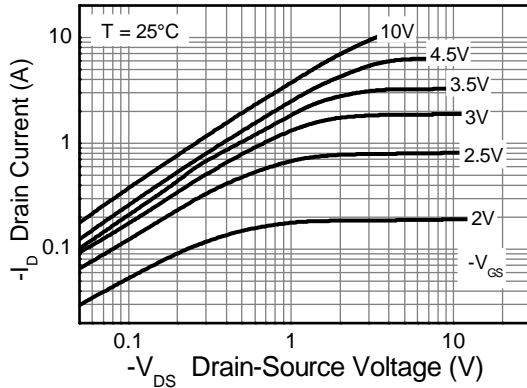


Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

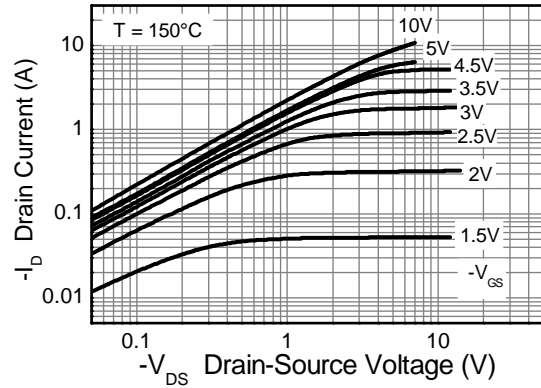
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	-60	—	—	V	I _D = -250μA, V _{GS} = 0V
Zero Gate Voltage Drain Current	I _{DSS}	—	—	-0.5	μA	V _{DS} = -60V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(th)}	-1.0	—	-3.0	V	I _D = -250μA, V _{DS} = V _{GS}
Static Drain-Source On-Resistance (Note 10)	R _{DS(on)}	—	—	0.400	Ω	V _{GS} = -10V, I _D = -0.9A
				0.600		V _{GS} = -4.5V, I _D = -0.8A
Forward Transconductance (Notes 10 and 12)	g _{fs}	—	1.8	—	S	V _{DS} = -15V, I _D = -0.9A
Diode Forward Voltage (Note 10)	V _{SD}	—	-0.85	-0.95	V	T _J = 25°C, I _S = -0.8A, V _{GS} = 0V
Reverse Recovery Time (Note 12)	t _{rr}	—	21.1	—	ns	T _J = 25°C, I _F = -0.9A,
Reverse Recovery Charge (Note 12)	Q _{rr}	—	19.3	—	nC	di/dt = 100A/μs
DYNAMIC CHARACTERISTICS (Note 12)						
Input Capacitance	C _{iss}	—	219	—	pF	V _{DS} = -30V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	25.7	—		
Reverse Transfer Capacitance	C _{rss}	—	20.5	—		
Turn-On Delay Time (Note 11)	t _{D(on)}	—	1.6	—	ns	V _{DD} = -30V, I _D = -1A, R _G ≅ 6.0Ω, V _{GS} = -10V
Turn-On Rise Time (Note 11)	t _r	—	2.2	—		
Turn-Off Delay Time (Note 11)	t _{D(off)}	—	11.2	—		
Turn-Off Fall Time (Note 11)	t _f	—	5.7	—		
Total Gate Charge (Note 11)	Q _g	—	2.9	—	nC	V _{DS} = -30V, V _{GS} = -4.5V, I _D = -0.9A
Total Gate Charge (Note 11)	Q _g	—	5.9	—	nC	V _{DS} = -30V, V _{GS} = -10V, I _D = -0.9A
Gate-Source Charge (Note 11)	Q _{gs}	—	0.74	—		
Gate-Drain Charge (Note 11)	Q _{gd}	—	1.5	—		

Notes: 10. Measured under pulsed conditions. Pulse width = 300μs. Duty cycle ≤ 2%.
 11. Switching characteristics are independent of operating junction temperature.
 12. For design aid only, not subject to production testing.

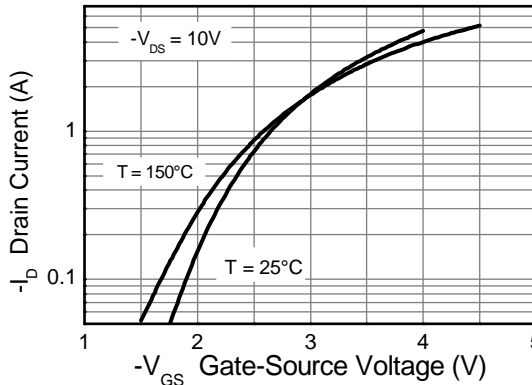
Typical Characteristics



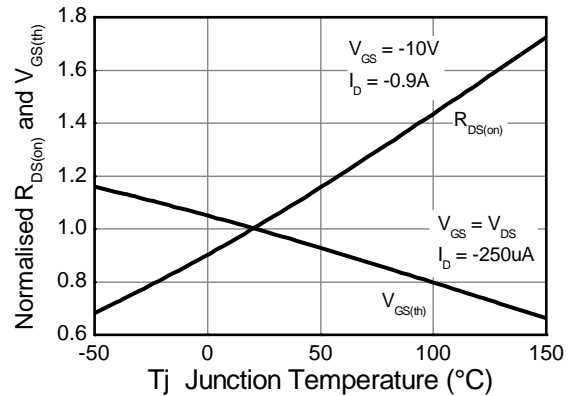
Output Characteristics



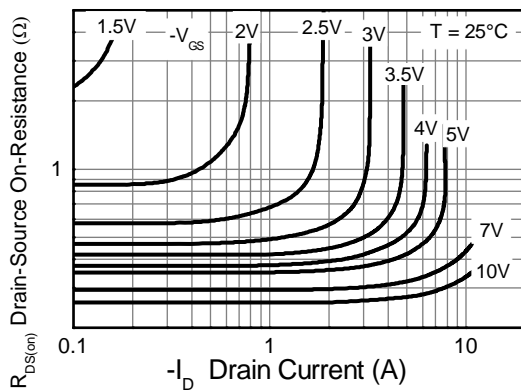
Output Characteristics



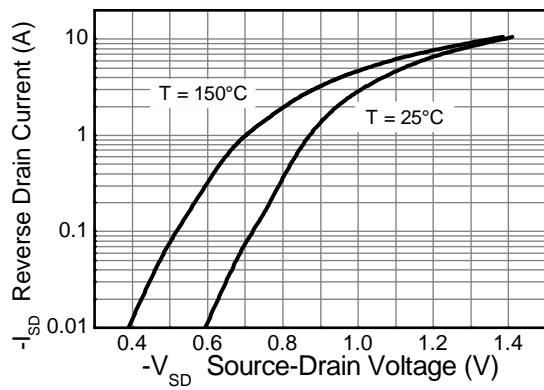
Typical Transfer Characteristics



Normalised Curves v Temperature

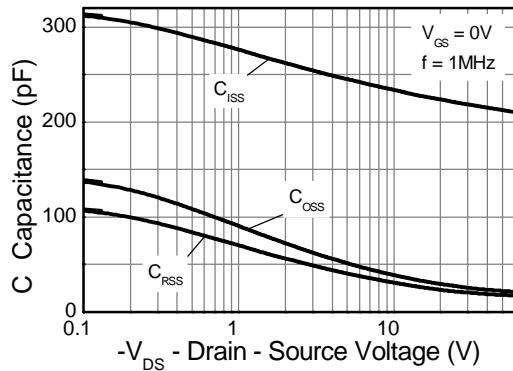


On-Resistance v Drain Current

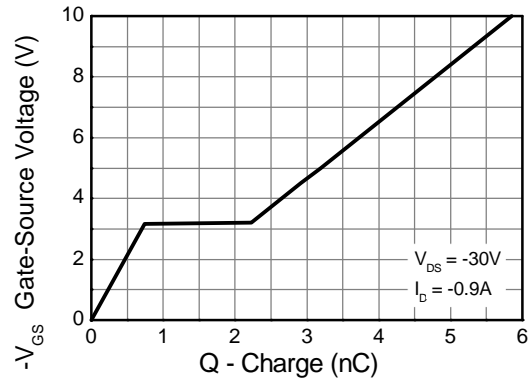


Source-Drain Diode Forward Voltage

Typical Characteristics - continued

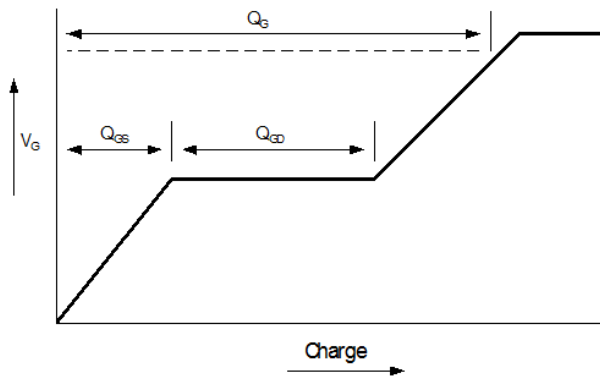


Capacitance v Drain-Source Voltage

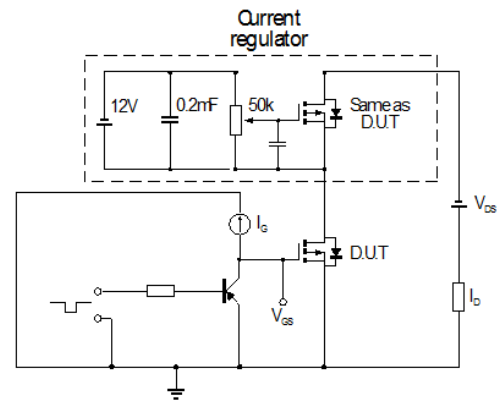


Gate-Source Voltage v Gate Charge

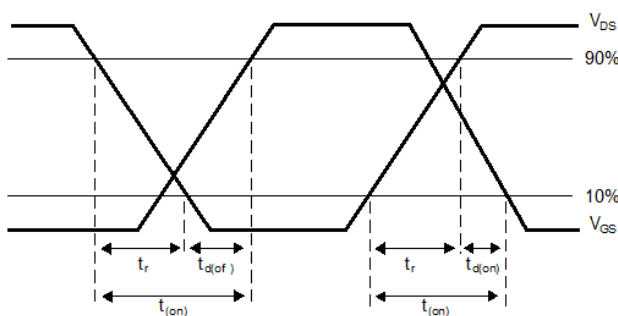
Test Circuits



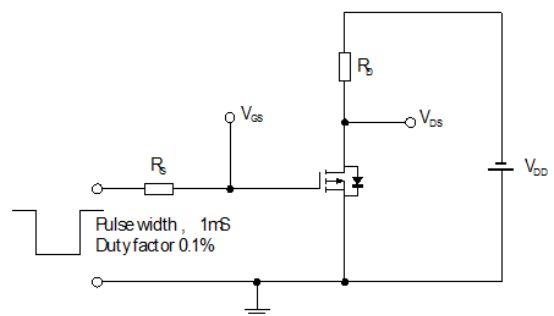
Basic gate charge waveform



Gate charge test circuit



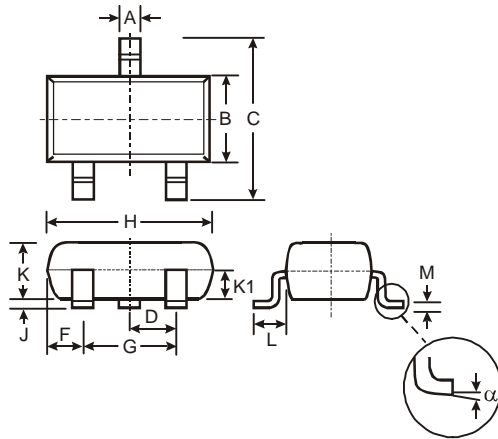
Switching time waveforms



Switching time test circuit

Package Outline Dimensions

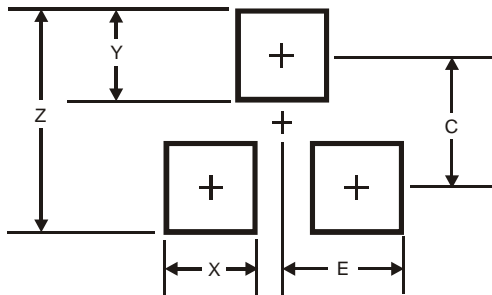
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.903	1.10	1.00
K1	-	-	0.400
L	0.45	0.61	0.55
M	0.085	0.18	0.11
α	0°	8°	-
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
Z	2.9
X	0.8
Y	0.9
C	2.0
E	1.35

IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDING TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

A. Life support devices or systems are devices or systems which:

1. are intended to implant into the body, or
2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.

B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2012, Diodes Incorporated

www.diodes.com