Medium Voltage Low Dissipation Factor Specifications and Test Methods

No.	Item		Specifications	Test Method	
1	Operating Temperature Range		-55 to +125°C	-	
2	Appearance		No defects or abnormalities	Visual inspection	
3	Dimensions		Within the specified dimension	Using calipers and micrometers	
4	Dielectric Strength		No defects or abnormalities	No failure should be observed when voltage in the Table is applied between the terminations for 1 to 5 sec., provided the charge/discharge current is less than 50mA. Rated Voltage DC200V, DC250V 200% of the rated voltage DC500V, DC630V 150% of the rated voltage DC1kV, DC2kV, 130% of the rated voltage	
5	Insulation Resistance (I.R.)		More than 10,000M Ω	The insulation resistance should be measured with rated voltage (DC500±50V in case of rated voltage: DC630V, DC1kV, DC2kV, DC3.15kV) and within 60±5 sec. of charging.	
6	Capacitar	nce	Within the specified tolerance	The capacitance/Q should be measured at the frequency and	
7	Q		1,000 min.	Voltage shown as follows. Capacitance Frequency Voltage C<1,000pF	
8	Capacitance Temperature Characteristics		Temp. Coefficient COG char. : 0±30ppm/°C (Temp. Range : +25 to +125°C) 0+30, -72ppm/°C (Temp. Range : -55 to +25°C) U2J char. : -750±120ppm/°C (Temp. Range : +25 to +125°C) -750+120, -347ppm/°C (Temp. Range : -55 to +25°C)	The capacitance measurement should be made at each step specified in the Table.StepTemperature (°C)125±22Min. Operating Temp.±3325±24Max. Operating Temp.±2525±2	
9	Adhesive Strength of Termination		No removal of the terminations or other defect should occur.	Solder the capacitor to the testing jig (glass epoxy board) shown in Fig. 1. Then apply 10N force in the direction of the arrow. The soldering should be done using the reflow method and should be conducted with care so that the soldering is uniform and free of defects such as heat shock. $\qquad \qquad $	
		Appearance	No defects or abnormalities	Solder the capacitor to the test jig (glass epoxy board).	
	Vibration Resistance	Capacitance	Within the specified tolerance	The capacitor should be subjected to a simple harmonic motion	
10		Q	1,000 min.	having a total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 55Hz. The frequency range, from 10 to 55Hz and return to 10Hz, should be traversed in approximately 1 min. This motion should be applied for a period of 2 hrs. in each of 3 mutually perpendicular directions (total of 6 hrs.).	

Continued on the following page.



Medium Voltage Low Dissipation Factor Specifications and Test Methods

Continued from the preceding page.

lo. I	tem	Specifications	Test Method
1 Deflecti	on	No marking defects fig. 2 fig. 3 fig.	Solder the capacitor to the testing jig (glass epoxy board) shown in Fig. 2. Then apply a force in the direction shown in Fig. 3. The soldering should be done using the reflow method and should be conducted with care so that the soldering is uniform and free of defects such as heat shock. $\underbrace{\begin{array}{c} 20 \\ Fressurize \\ \hline \\ Capacitance meter \\ 45 \\ \hline \end{array}}_{Fig. 3}^{50} Fressurize \\ (in mm) \\ Fig. 3 \\ \hline \end{array}$
I2 Soldera Termina	bility of tion	75% of the terminations are to be soldered evenly and continuously.	Immerse the capacitor in a solution of ethanol (JIS-K-8101) and rosin (JIS-K-5902) (25% rosin in weight proportion). Immerse in solder solution for 2±0.5 sec. Immersing speed: 25±2.5mm/s Temp. of solder: 245±5°C Lead Free Solder (Sn-3.0Ag-0.5Cu) 235±5°C H60A or H63A Eutectic Solder
	Appearance	No marking defects	Preheat the capacitor at 120 to 150°C* for 1 min.
Resistance		Within ±2.5%	 Immerse the capacitor in solder solution at 260±5°C for 10±1 sec. Let sit at room condition* for 24±2 hrs., then measure. Immersing speed: 25±2.5mm/s
3 to Solderin	·	1,000 min.	*Preheating for more than 3.2×2.5mm
Heat	I.R.	More than 10,000MΩ	Step Temperature Time
	Dielectric Strength	In accordance with item No.4	1 100 to 120°C 1 min. 2 170 to 200°C 1 min.
	Appearance	No marking defects	Fix the capacitor to the supporting jig (glass epoxy board) shown
	Capacitance Change	Within ±2.5%	in Fig. 4. Perform the 5 cycles according to the 4 heat treatments listed in the following table.
	Q	500 min.	Let sit for 24±2 hrs. at room condition,* then measure.
	I.R.	More than 10,000MΩ	Step Temperature (°C) Time (min.) 1 Min. Operating Temp.±3 30±3
4 Temperature Cycle	Dielectric Strength	In accordance with item No.4	2 Room Temp. 2 to 3 3 Max. Operating Temp.±2 30±3 4 Room Temp. 2 to 3 4 Room Temp. 2 to 3 5 10 10 10 10 10 11 10 10 12 12 12 12 12 12 12 12 12 12 12 12 13 10 10 14 10 10 15 10 10 16 10 10 17 10 10 18 10 10 19 10 10 10 10 10 10 10 10 10 10 10 10 10 10 10 10 10 10 10 10 10 10 10 10 10 10 10 10 10
	Appearance	No marking defects	
Humidity	Capacitance Change	Within ±5.0%	Let the capacitor sit at 40±2°C and relative humidity of 90 to 95%
5 (Steady	Q	350 min.	for $500^{\pm 20}$ hrs. Remove and let sit for 24±2 hrs. at room condition,* then
State)	I.R.	More than 1,000MΩ	measure.
	Dielectric Strength	In accordance with item No.4	
	Appearance	No marking defects	Apply voltage as in Table for 1,000 ^{±48} hrs. at maximum
	Capacitance Change	Within ±3.0%	operating temperature ±3°C. Remove and let sit for 24±2 hrs. at room condition,* then measure.
	Q	350 min.	Rated Voltage Applied Voltage
6 Life	I.R. Dielectric	More than 1,000MΩ In accordance with item No.4	DC200V, DC250V 150% of the rated voltage DC500V, DC630V, DC1kV, DC2kV, 120% of the rated voltage DC3.15kV
	Strength	In accordance with item No.4	DC3.15kV The charge/discharge current is less than 50mA.

* "Room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa

